

**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

U.S. GOVERNMENT					
APPL. NO.	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10071453	02/08/2002	438	275	2813	SCHILLINGER

PATENT AGENTS: Tran Tuan; [Signature]

**CONTINUING DATA VERIFIED:
THIS APPLICATION IS A DIV OF 09/334,660 04/12/2001
WHICH IS A DIV OF 09/382,357 09/01/1999 [Signature]

OTHER PENDING APPLICATIONS VERIFIED: [Signature]

☐ PUBLISHED IN FULL ☒ REISSUED

☐ CONTAINS CLAIMS ☐ YES ☒ NO

☐ CONTAINS DRAWINGS ☐ YES ☒ NO

Attorney's Name and Address: [Blank]
Examiner's Initials: [Blank]

ATTORNEY DOCKET NO.
M122-1021

Abstract: Semiconductor processing methods of forming transistors, semiconductor processing method of forming random access memory circuitry, and related integrated circuitry.

U.S. DEPT. OF COMMERCE PAT. & TRADEMARK OFFICE

NOTICE OF ALLOWANCE	
8/23/0	
ISSUE	
Amount Due	
\$ 1580.00	
<input type="checkbox"/>	TERM

DRAWINGS			CLAIMS ALLOWED	
Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
_____ (Assistant Examiner)			NOTICE OF ALLOWANCE MAILED	
			_____ (Date)	
_____ (Primary Examiner)			ISSUE FEE	
			Amount Due	Date Paid
_____ (Legal Instruments Examiner)			ISSUE BATCH NUMBER	
_____ (Date)			_____ (Date)	